## REMARKS/ARGUMENTS

Claims 1 - 4 have been cancelled, and new claims 6 and 7 have been added. Claim 5 has been amended.

Claims 5 - 7 are pending in the application.

The specification has been amended on page 8 to correct a typographical error.

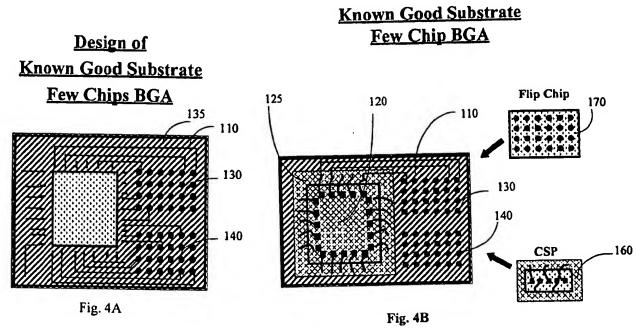
The rejection of claim 5 under 35 U.S.C. §102(e) as being anticipated by Lin (US 6,677,668) is respectfully traversed. Lin relates to an integration of multiple electronic components into a single package.

In contrast, in the case of applicant's tailored interconnect module, the majority of the nodes on the peripheral devices are designed to interface with nodes of one or more central devices in such a way as to condense the number of nets so that the total number of nodes connected to external pins is minimized. The same is true for power and ground pins of the device. Rather than simply supplying each device in question, they can pooled to a set of local reference planes for distribution to the other devices from the central and localized reference planes. In the extreme, this approach can be leveraged to the point where all interconnect will be contained locally and the number of pins exiting the package could be reduced as low as one bidirectional pin. (See pages 3 and 4 of the specification.)

Each integrated circuit device has mounted inside the package a plurality of interface ports and at least one interface port

being connected to one of a plurality of circuit devices. At least one of the integrated circuit devices has an interface port connected to an interface pin.

The Examiner refers to Figs. 4A and 4B which are reproduced in reduced form herebelow for convenience of reference:



## Top View

Clearly, the applicant's tailored interconnect module as defined in claim 5 is not shown in Figs. 4A and 4B; is not referred elsewhere in Lin; and is not made obvious thereby.

New claims 6 and 7 depend from claim 5 and are allowable for the same reasons.

In view of the above, further and favorable reconsideration is respectfully requested.

Respectfully submitted,

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